



PATENT

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Shirley A. Hester

Applicant : Jun Cao
Application No. : 09/784,419
Filed : February 15, 2001
Title : LINEAR FULL RATE PHASE DETECTOR AND CLOCK DATA
RECOVERY CIRCUIT

Date of Notice
of Allowance : May 28, 2004

Grp./Div. : 2633
Examiner : David C. Payne

Docket No. : 50987/RJP/B600

TRANSMITTAL OF DRAWINGS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
August 12, 2004

Commissioner:

Enclosed are 11 sheets of drawings for this application. They are submitted as formal drawings to replace the corresponding informal drawings as originally filed.

Respectfully submitted,

CHRISTIE, PARKER & HALE, LLP

By

Colin Dorrian

Colin Dorrian
Reg. No. 54,658
626/795-9900

CTD/srh

Enclosures: As set forth above.

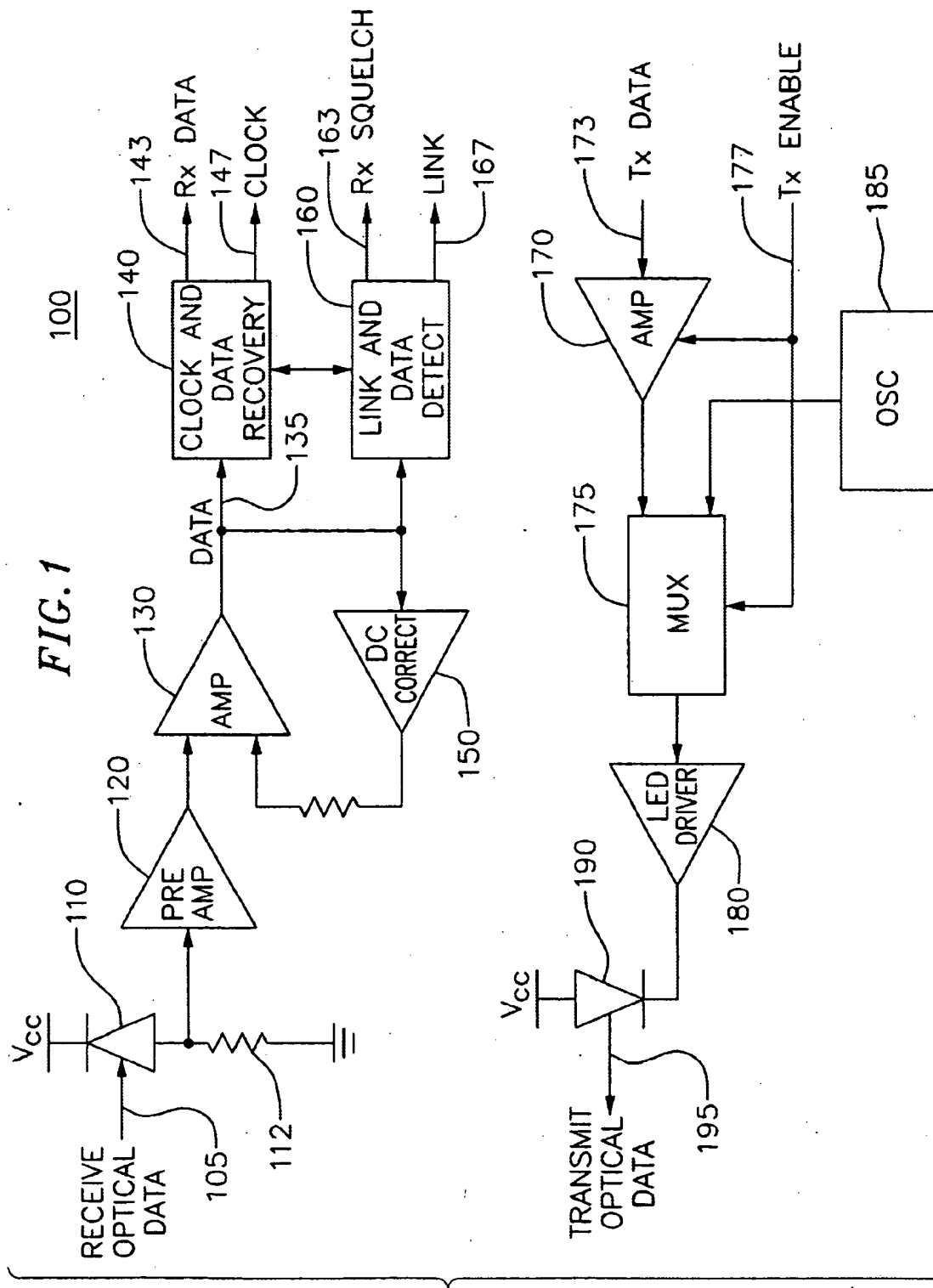
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691784, 419

6909852

FIG. 1



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FIG. 2

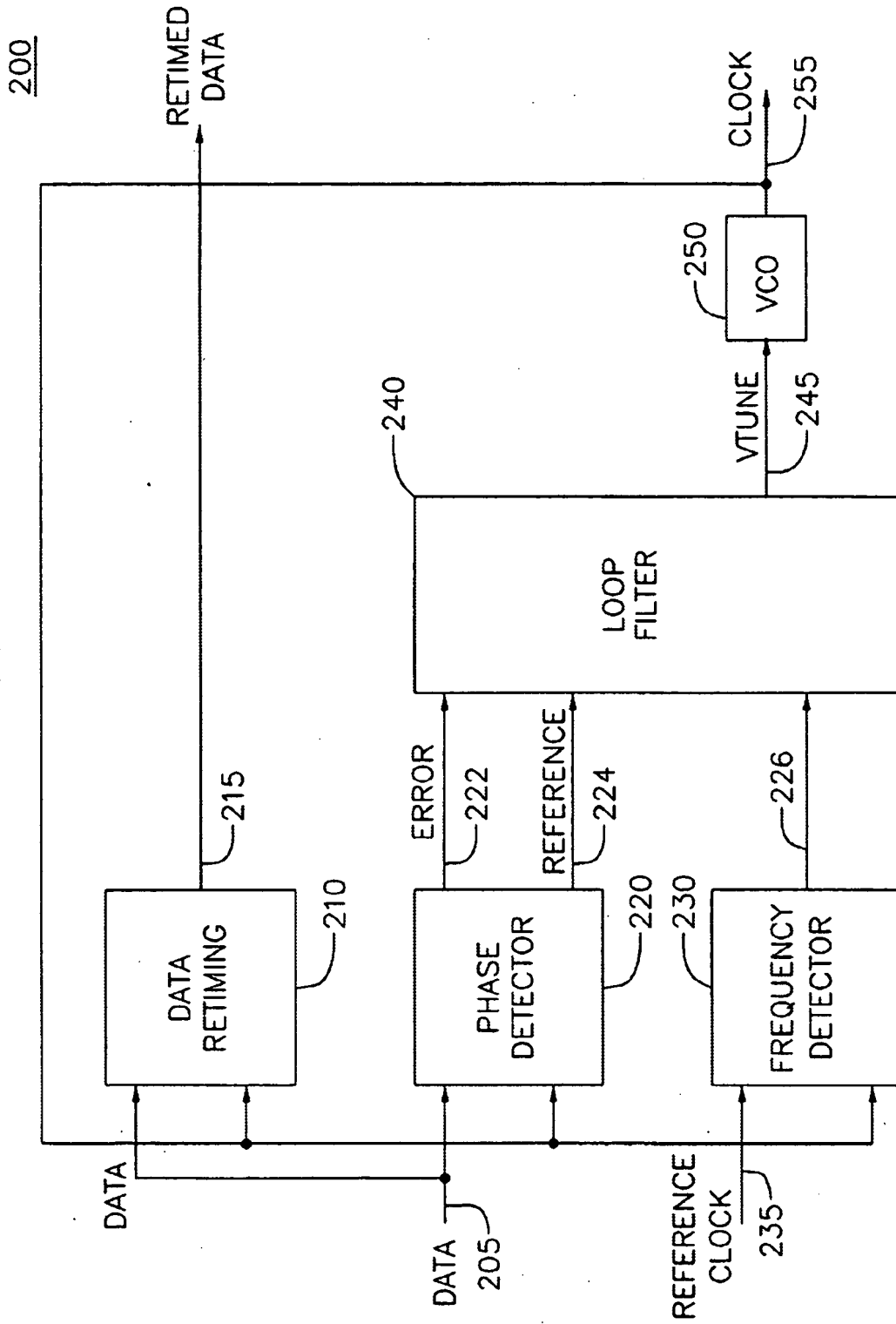
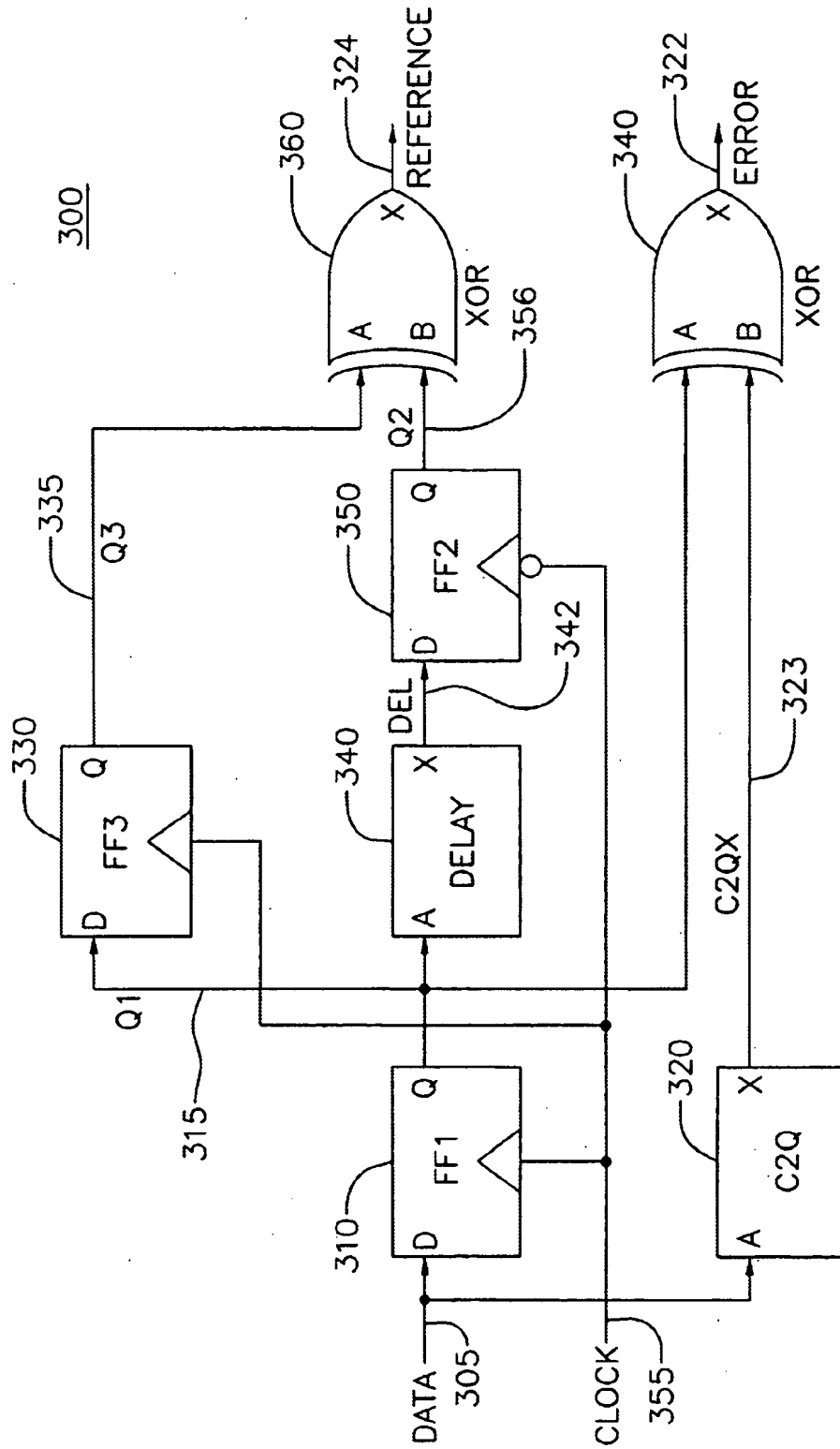


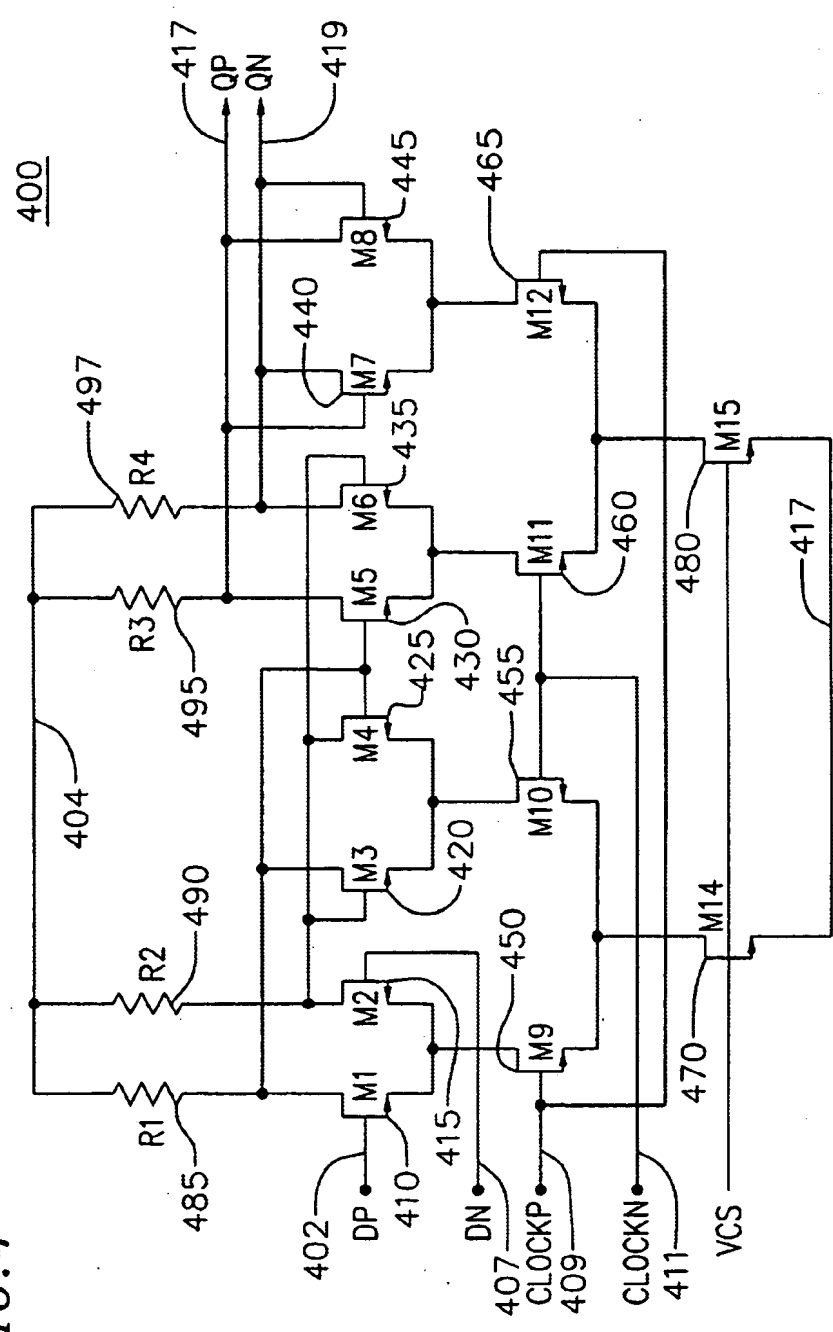


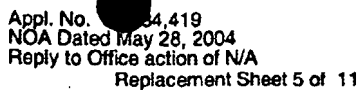
FIG. 3



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FIG. 4





500

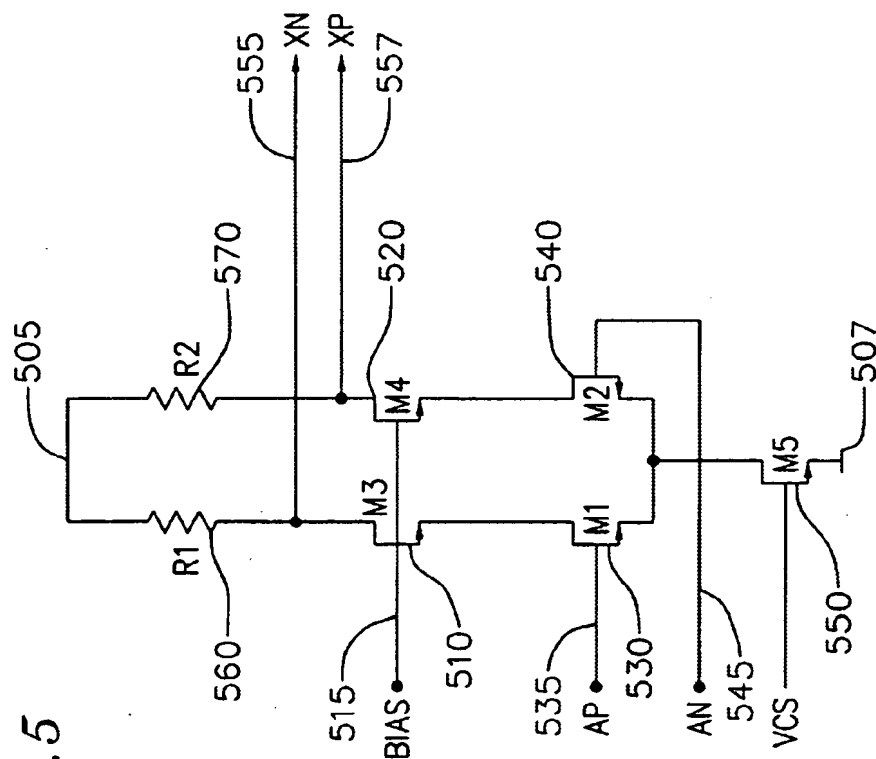
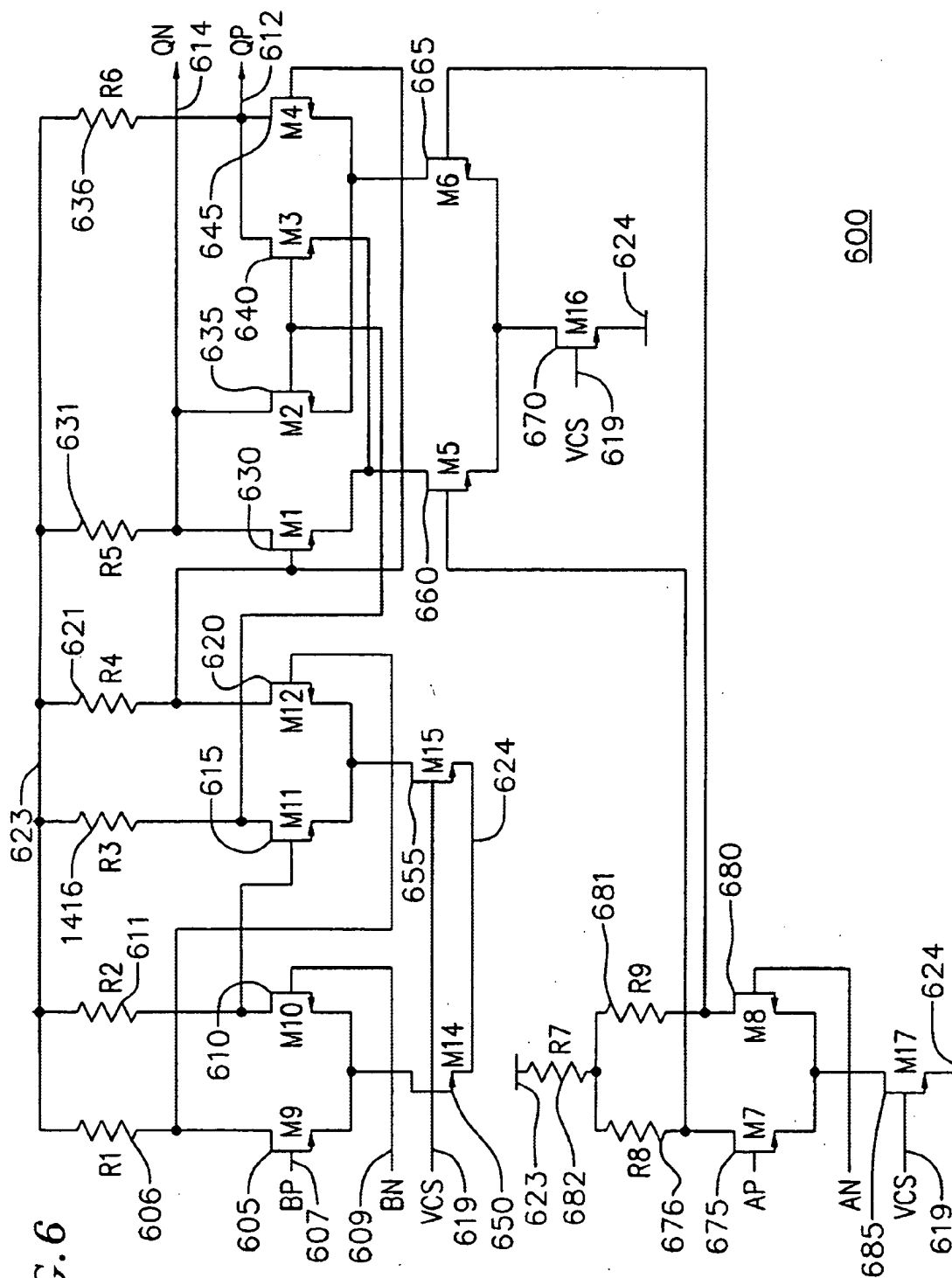




FIG. 6



600



FIG. 7

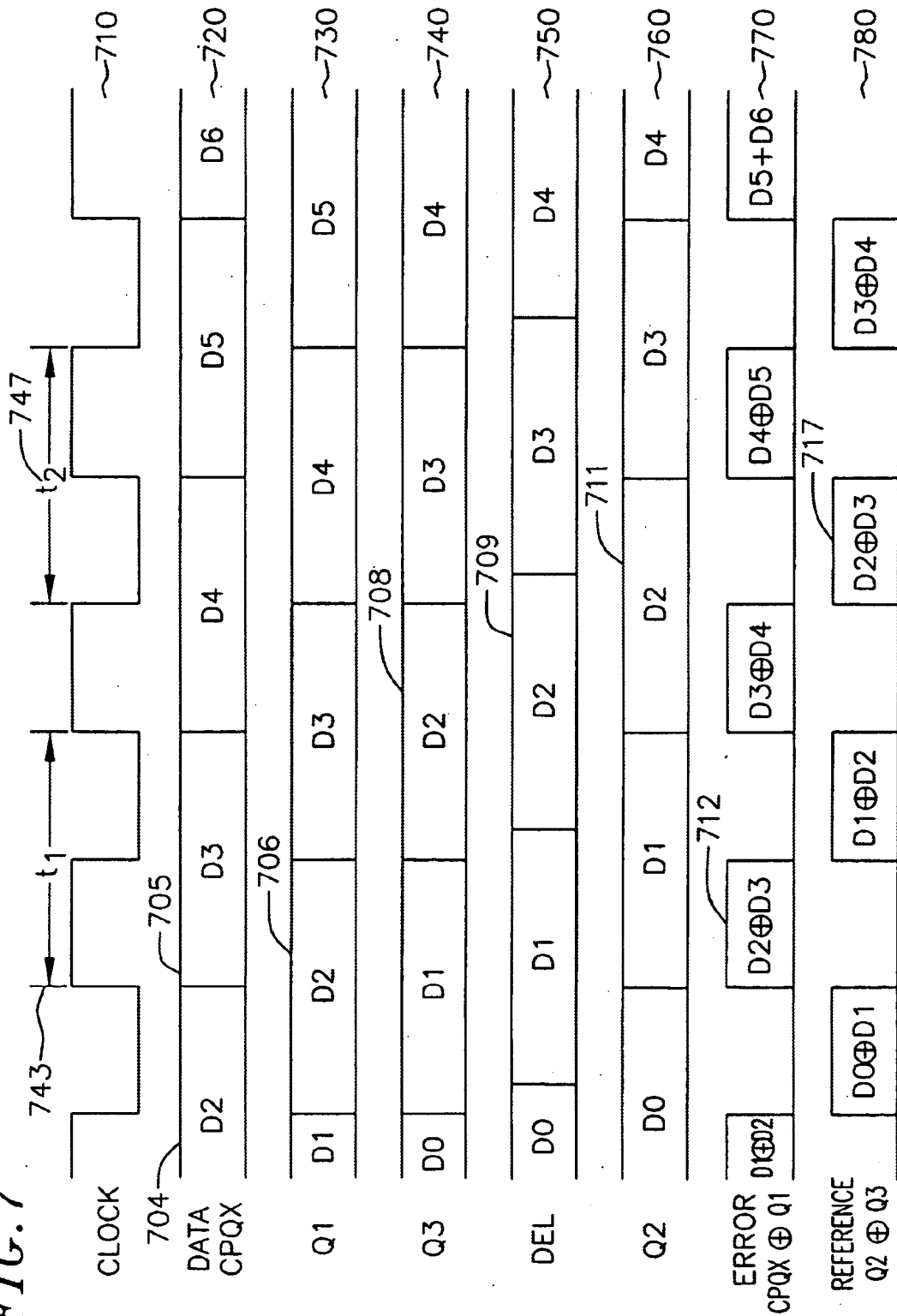




FIG. 8

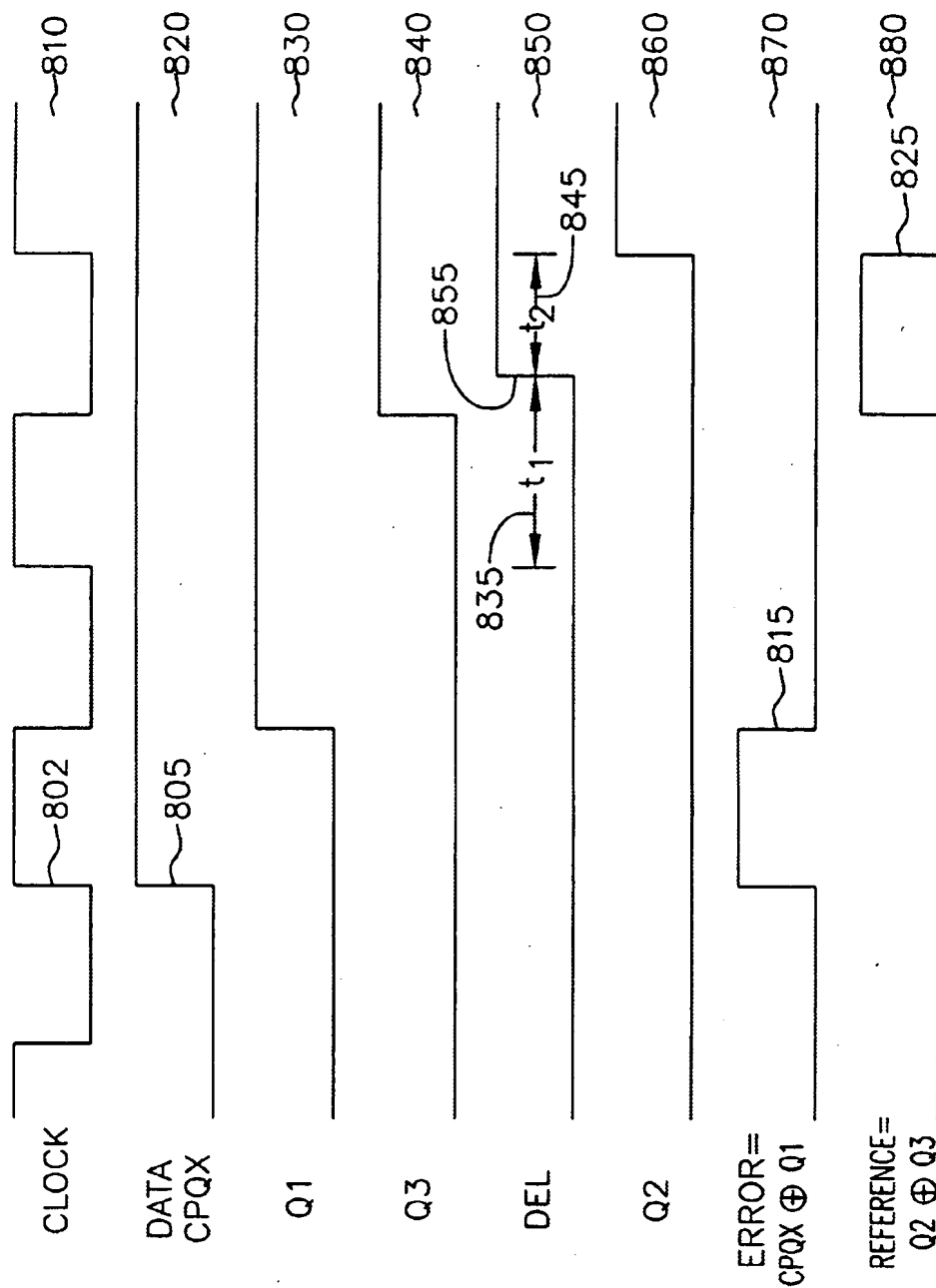




FIG. 9

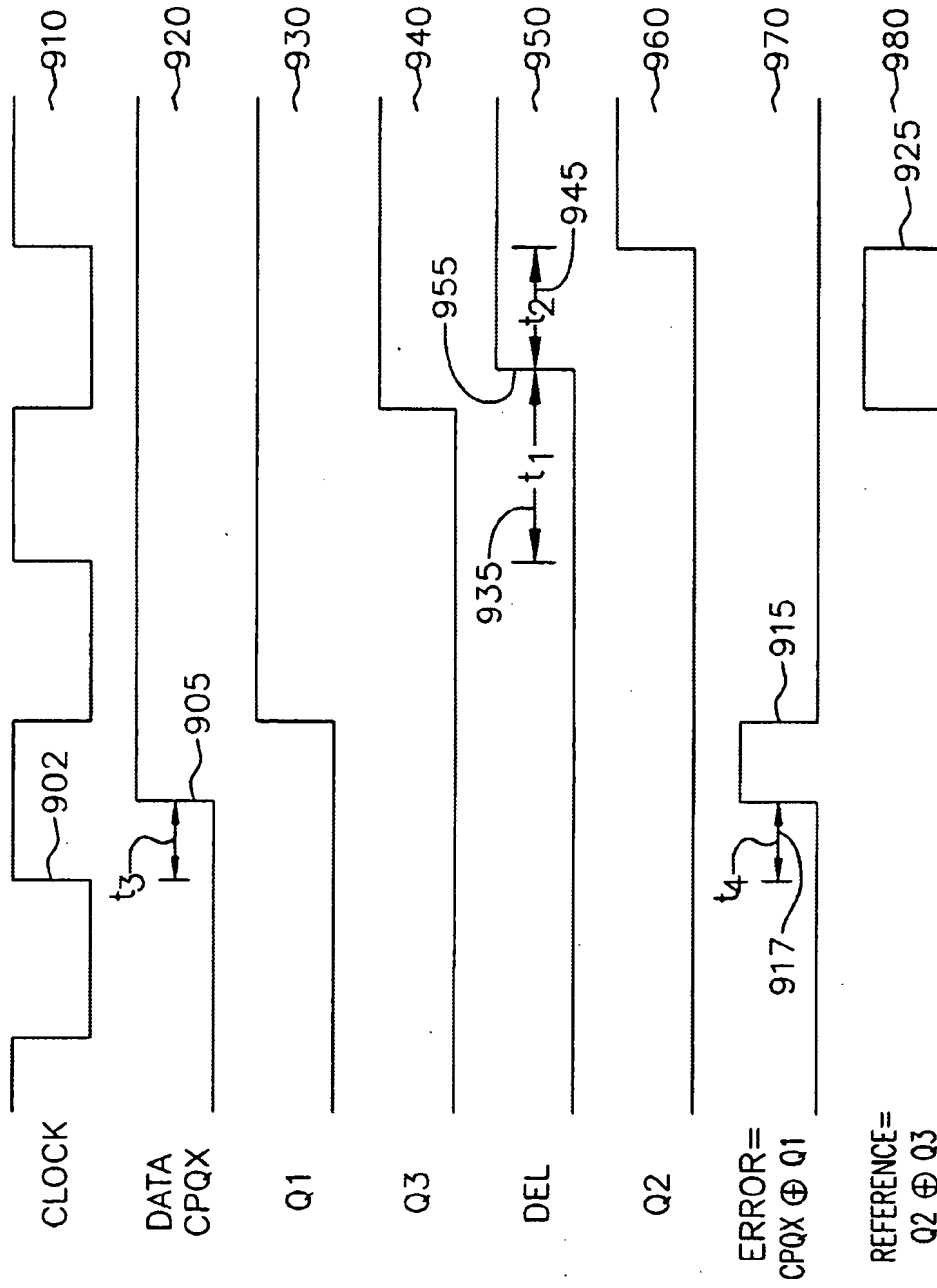




FIG. 10

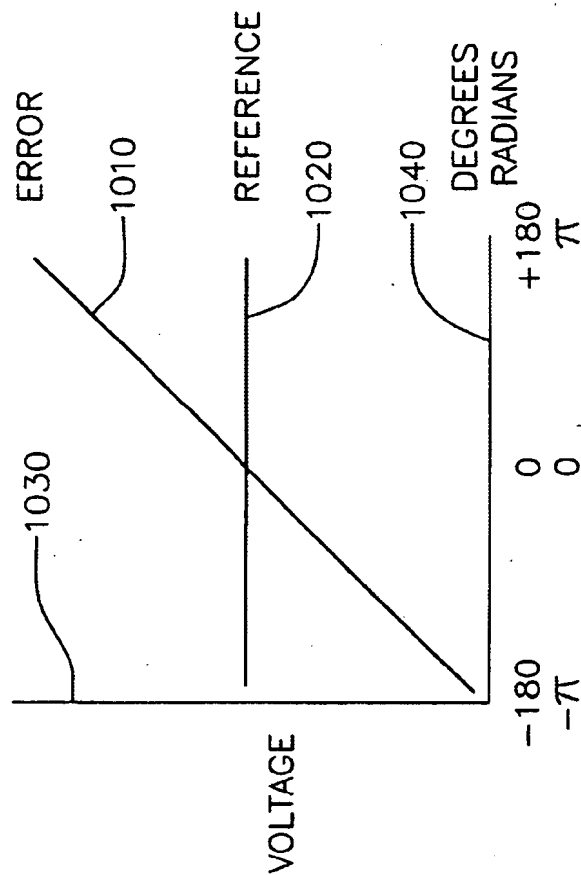




FIG. 11

1100

